Appl No. 09/814044

PATENT IBM Docket No. RAL920000083US1

Amendments to the Specification:

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Amend page 1, paragraph beginning at line 18, as follows:

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The invention contemplates a low power DSL modern transmitter, suitable for incorporation in integrated DSLAM server line cards, in which full power physical frames including a control channel signal component and a data signal component are interspersed with physical frames having only a control channel signal component or a include a control channel signal component and a data signal component and low power physical frames include a control channel signal component and a low power synchronization signal component.

Amend page 5, paragraph beginning at line 9, as follows:

This is accomplished with the following additional components. A control channel generating circuit 20, in response to signals from block 11 (indicating that there is no data for transmission to the client), initiates the generation of a second physical frame which includes as a minimum a control channel which is identical in format to the control channel generated in block 11 when 11, when it has data to send to the client. In addition to the control channel it may, in most circumstance, be desirable to include a low power synchronization signal in the physical frame initiated in control channel 20. The second physical frame initiated in control channel 20 is applied to a block [[13']] 13' which is structurally identical to block 13. The outputs of blocks 14 and [[14']] 14' are selected for transmission by a frame selector 21 which is controlled by a control signal provided by control channel 20 over a conductor 22. The remainder of the transmitter is identical to the transmitter illustrated in Figure 1.